

ATTORNEY'S DOCKET NO: 99-TK-239 (JHM/EJR)
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: David A. Edwards, et al.
Serial No: 09/410,606
Filed: October 1, 1999
For: APPARATUS AND METHOD FOR SHADOWING PROCESSOR
INFORMATION
Examined: Not Yet Assigned
Art Unit: 2785

Commissioner for Patents
Washington, D.C. 20231

Sir:

Transmitted herewith are the following documents:

- ☒ Information Disclosure Statement;
- ☒ Form PTO-1449 and References; and
- ☒ Return Receipt Postcard

If the enclosed papers are considered incomplete, the Mail Room and/or the Application Branch is respectfully requested to contact the undersigned at (617) 720-3500, Boston, Massachusetts.

No fee is enclosed or required. If the fee is insufficient, the balance may be charged to the account of the undersigned, Deposit Account No. 23/2825. A duplicate of this sheet is enclosed.

Respectfully submitted,
David A. Edwards, et al., Applicant(s)

By: 

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Docket No. 99-TK-239 (JHM/EJR)
Dated: July 25, 2000
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STATEMENT FILED PURSUANT TO THE DUTY OF
DISCLOSURE UNDER 37 CFR §§1.56, 1.97 AND 1.98

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

PART I: Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement has been filed before the mailing date of a first Office Action on the merits in the above-identified case.

No fee or certification is required.

PART II: - Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

The Applicant would like to bring to the Examiner's attention the following co-pending applications that may contain subject matter related to this application:

<u>Serial No.</u>	<u>Filing Date</u>	<u>Title of Application</u>
09/411,792	October 1, 1999	Interface for Transferring Debug Information
09/410,558	October 1, 1999	Apparatus and Method for Storing Trace Information
09/410,560	October 1, 1999	Circuit for Storing Trace Information
09/409,612	October 1, 1999	Circuit for Processing Trace Information

<u>Serial No.</u>	<u>Filing Date</u>	<u>Title of Application</u>
09/411,794	October 1, 1999	Method for Compressing and Decompressing Trace Information
09/410,732	October 1, 1999	System and Method for Communicating with an Integrated Circuit
09/410,638	October 1, 1999	System and Method for Communicating with an Integrated Circuit
09/411,815	October 1, 1999	System and Method for Communicating with an Integrated Circuit
09/410,860	October 1, 1999	System and Method for Communicating with an Integrated Circuit
09/411,614	October 1, 1999	Data Shift Register
09/411,795	October 1, 1999	System and Method for Communicating with an Integrated Circuit
09/410,607	October 1, 1999	Microcomputer Debug Architecture and Method
09/410,734	October 1, 1999	Microcomputer Debug Architecture and Method
09/411,786	October 1, 1999	Microcomputer Debug Architecture and Method
09/411,146	October 1, 1999	Microcomputer Debug Architecture and Method
09/411,608	October 1, 1999	Microprocessor/Floating Point Processor Interface and Method
09/410,925	October 1, 1999	Microprocessor/Floating Point Processor Interface and Method
09/410,637	October 1, 1999	Microprocessor/Floating Point Processor Interface and Method
09/410,604	October 1, 1999	Microcomputer/Floating Point Processor Interface and Method

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PART III: Remarks

A copy of each of the above-identified information is enclosed unless otherwise indicated on the attached form PTO-1449 (modified). It is respectfully requested that:

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;

3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicant makes no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

Respectfully submitted,
David A. Edwards, et al., Applicant(s)

By: 

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